

INFORMATION DISCLOSURE CITATION PTO-1449		MAIL ROOM		EXAMINER'S ROOM		
		ATTORNEY'S DKT NO. 018414-148		APPLICATION NO. 08/480,739		
		APPLICANT William K. ZURAVLEFF et al.				
FILING DATE June 7, 1995		GROUP 2317				
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>JKR</i>	4,833,655	05/23/89	Michnel A. Wolf et al.	365	221	06/28/85
<i>JKR</i>	5,179,688	01/12/93	Douglas B. Brown et al.	395	425	06/30/87
<i>JKR</i>	5,208,490	05/04/93	Jeffry D. Yetter	307	452	04/12/91
<i>JKR</i>	5,249,297	09/28/93	Russell C. Brockmann et al.	395	725	04/29/91
<i>JKR</i>	5,257,356	10/26/93	Russell C. Brockmann et al.	395	725	05/28/91
<i>JKR</i>	5,289,403	02/22/94	Jeffry D. Yetter	365	49	07/08/91
<i>JKR</i>	5,299,158	03/29/94	Russell W. Mason et al.	365	189.04	06/16/92
<i>JKR</i>	5,317,204	05/31/94	Jeffry D. Yetter	307	443	05/19/92
<i>JKR</i>	5,329,176	07/12/94	Robert H. Miller, Jr. et al.	307	443	09/29/93
<i>JKR</i>	5,343,096	08/30/94	Craig A. Heikes et al.	307	480	05/19/92
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>JKR</i>	Eric DeLano et al., "A High Speed Superscaler PA-RISC Processor", IEEE, pp.116-121, 1992					
<i>JKR</i>	Doug Hunt, "Advanced Performance Features of the 64-bit PA-8000", IEEE, pp.123-128, 1995					
<i>JKR</i>	Jonathan Lotz et al., "A CMOS RISC CPU Designed for Sustained High Performance on Large Applications", IEEE Journal of Solid-State Circuits, vol. 25, pp.1190-1198, Oct. 1990					
<i>JKR</i>	William Jaffe et al., "A 200MFLOP Precision Architecture Processor", Hewlett-Packard, pp.1.2.1-1.2.13					
EXAMINER <i>JKR</i>		DATE CONSIDERED <i>11/14/96</i>				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.